

**Spring Semester**

 **May 2024 Exam Timetable**

**School of Engineering**

**Department of Electronic Engineering**

**Important notice for all students:**

**Timetables are subject to change - revisit this site for updates**

* **Please ensure that you are in possession of a current photographic ID card**

**If not, you should contact the Student Hub as soon as possible**

* **NBA = National Basketball Arena**
* **TU Dublin – Tallaght Campus**

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| **BACHELOR OF ENGINEERING – ELECTRONIC ENGINEERING GENERAL ENTRY** **– AB INITIO - YEAR 1** |
| **Timetable** | **Course Code** | **TA\_ELEGEN\_D - Year 1** | **FT ONLY** |  |
| **DATE** | **DAY** | **SUBJECT** | **TIME** | **LOCATION**  |
| **19th August** | **Monday** | **Mathematics 2** | **15:30** | **146/148** |
| **22nd August** | **Thursday** | **Electrical Circuits 2** | **12:30** | **142/144** |
| **23rd August** | **Friday** | **Interactive Embedded Systems** | **15:30** | **146/148** |
| **24th August** | **Saturday** | **Analogue Electronics** | **12:30** | **142/144** |
| **BACHELOR OF ENGINEERING(HONS) – ELECTRONIC ENGINEERING GENERAL ENTRY** **– YEAR 1** |
| **Timetable** | **Course Code** | **TA\_ELEGEN\_B - Year 1** | **FT ONLY** |  |
| **DATE** | **DAY** | **SUBJECT** | **TIME** | **LOCATION**  |
| **19th August** | **Monday** | **Mathematics 2** | **15:30** | **146/148** |
| **22nd August** | **Thursday** | **Electrical Circuits 2** | **12:30** | **142/144** |
| **23rd August** | **Friday** | **Interactive Embedded Systems** | **15:30** | **146/148** |
| **24th August** | **Saturday** | **Analogue Electronics** | **12:30** | **142/144** |

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| **HIGHER CERTIFICATE IN ENGINEERING – ELECTRONIC & INFORMATION ENGINEERING - YEAR 1** |
| **Timetable** | **Course Code** | **TA\_EELIE\_C - Year 1** |  |  |
| **DATE** | **DAY** | **SUBJECT** | **TIME** | **LOCATION**  |
| **19th August** | **Monday** | **Mathematics 2** | **15:30** | **146/148** |
| **20th August** | **Tuesday** | **Digital Systems 1** | **19:00** | **142/144** |
| **22nd August** | **Thursday** | **Electrical Circuits 2** | **12:30** | **142/144** |
| **23rd August** | **Friday** | **Interactive Embedded Systems** | **15:30** | **146/148** |
| **24th August** | **Saturday** | **Analogue Electronics** | **12:30** | **142/144** |

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| **HIGHER CERTIFICATE IN ENGINEERING – ELECTRONIC ENGINEERING - YEAR 1** |
| **Timetable** | **Course Code** | **TA\_EELEC\_C - Year 1**  | **PT ONLY** |
| **DATE** | **DAY** | **SUBJECT** | **TIME** | **LOCATION**  |
| **19th August** | **Monday** | **Mathematics 2** | **15:30** | **146/148** |
| **20th August** | **Tuesday** | **Digital Systems 1** | **19:00** | **142/144** |
| **22nd August** | **Thursday** | **Electrical Circuits 2** | **12:30** | **142/144** |
| **23rd August** | **Friday** | **Interactive Embedded Systems** | **15:30** | **146/148** |

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| **HIGHER CERTIFICATE IN ENGINEERING – ELECTRONIC & INFORMATION ENGINEERING** |
| **Timetable** | **Course Code** | **TA\_EELIE\_C - Year 2** | **Full Time ONLY** |
| **DATE** | **DAY** | **SUBJECT** | **TIME** | **LOCATION** |
| **19th August** | **Monday** | **Mathematics 4** | **09:30** | **46** |
| **20th August** | **Tuesday** | **Microprocessor Fundamentals** | **15:30** | **050/052** |
| **21st August** | **Wednesday** | **Solid State Electronics (e)** | **15:30** | **050/052** |
| **21st August** | **Wednesday** | **GUI Development (e)** | **19:00** | **223/225** |
| **22nd August** | **Thursday** | **Routers & Switches** | **15:30** | **142/144** |
| **24th August** | **Saturday** | **Control Systems** | **09:30** | **040/042** |
| **HIGHER CERTIFICATE IN ENGINEERING - ELECTRONIC ENGINEERING - YEAR 2** |
| **Timetable** | **Course Code** | **TA\_EELEC\_C** | **Part Time Only** |
| **DATE** | **DAY** | **SUBJECT** | **TIME** | **LOCATION** |
| **19th August** | **Monday** | **Mathematics 4** | **09:30** | **46** |
| **21st August** | **Wednesday** | **Solid State Electronics (e)** | **15:30** | **050/052** |
| **21st August** | **Wednesday** | **GUI Development (e)** | **19:00** | **223/225** |
| **24th August** | **Saturday** | **Control Systems** | **09:30** | **040/042** |

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| **BACHELOR OF ENGINEERING - ELECTRONIC ENGINEERING – AB INITIO - YEAR 2** |
| **Timetable** | **Course Code** | **TA\_EAELE\_D - Year 2** | **Full Time ONLY** |
| **DATE** | **DAY** | **SUBJECT** | **TIME** | **LOCATION** |
| **19th August** | **Monday** | **Mathematics 4** | **09:30** | **46** |
| **20th August** | **Tuesday** | **Microprocessor Fundamentals** | **15:30** | **050/052** |
| **21st August** | **Wednesday** | **Solid State Electronics (e)** | **15:30** | **050/052** |
| **21st August** | **Wednesday** | **GUI Development (e)** | **19:00** | **223/225** |
| **22nd August** | **Thursday** | **Routers & Switches** | **15:30** | **142/144** |
| **24th August** | **Saturday** | **Control Systems** | **09:30** | **040/042** |

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| **BACHELOR OF ENGINEERING (HONS) - ELECTRONIC ENGINEERING – AB INITIO- YEAR 2** |
| **Timetable** | **Course Code** | **TA\_EAELE\_B - Year 2** | **Full Time ONLY** |
| **DATE** | **DAY** | **SUBJECT** | **TIME** | **LOCATION** |
| **19th August** | **Monday** | **Mathematics 4** | **09:30** | **46** |
| **20th August** | **Tuesday** | **Microprocessor Fundamentals** | **15:30** | **050/052** |
| **21st August** | **Wednesday** | **Solid State Electronics (e)** | **15:30** | **050/052** |
| **21st August** | **Wednesday** | **GUI Development (e)** | **19:00** | **223/225** |
| **22nd August** | **Thursday** | **Routers & Switches** | **15:30** | **142/144** |
| **24th August** | **Saturday** | **Control Systems** | **09:30** | **040/042** |

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| **BACHELOR OF ENGINEERING – ENGINEERING SOFTWARE –AB INITIO YEAR 2** |
| **Timetable** | **Course Code** | **EAENS\_D Year 2** | **Full Time ONLY** |
| **DATE** | **DAY** | **SUBJECT** | **TIME** | **LOCATION** |
| **19th August** | **Monday** | **Mathematics 4** | **09:30** | **46** |
| **20th August** | **Tuesday** | **Microprocessor Fundamentals** | **15:30** | **050/052** |
| **21st August** | **Wednesday** | **Solid State Electronics (e)** | **15:30** | **050/052** |
| **21st August** | **Wednesday** | **GUI Development (e)** | **19:00** | **223/225** |
| **22nd August** | **Thursday** | **Routers & Switches** | **15:30** | **142/144** |
| **24th August** | **Saturday** | **Control Systems** | **09:30** | **040/042** |

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| **BACHELOR OF ENGINEERING (HONS) – ENGINEERING SOFTWARE –AB INITIO** |
| **Timetable** | **Course Code** | **TA\_EAENS\_B Year 2** |  |
| **DATE** | **DAY** | **SUBJECT** | **TIME** | **LOCATION** |
| **19th August** | **Monday** | **Mathematics 4** | **09:30** | **46** |
| **20th August** | **Tuesday** | **Microprocessor Fundamentals** | **15:30** | **050/052** |
| **21st August** | **Wednesday** | **Solid State Electronics (e)** | **15:30** | **050/052** |
| **21st August** | **Wednesday** | **GUI Development (e)** | **19:00** | **223/225** |
| **22nd August** | **Thursday** | **Routers & Switches** | **15:30** | **142/144** |
| **24th August** | **Saturday** | **Control Systems** | **09:30** | **040/042** |

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| **BACHELOR OF ENGINEERING - ELECTRONIC ENGINEERING - YEAR 3**  |
| **Timetable** | **Course Code** | **TA\_EELEC\_D - Year 3** | **Full Time & Part Time** |
| **DATE** | **DAY** | **SUBJECT** | **TIME** | **LOCATION**  |
| **19th August** | **Monday** | **Mathematics 6** | **15:30** | **146/148** |
| **20th August** | **Tuesday** | **Analysis of Analogue Communications** | **09:30** | **223/225** |
| **21st August** | **Wednesday** | **Electronic Circuits & Filters** | **12:30** | **138/140** |
| **22nd August** | **Thursday** | **Power Engineering** | **15:30** | **142/144** |
| **23rd August** | **Friday** | **Embedded Systems** | **09:30** | **040/042** |
| **24th August** | **Saturday** | **Digital Design with Verilog** | **09:30** | **040/042** |

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| **BACHELOR OF ENGINEERING - ELECTRONIC ENGINEERING – AB INITIO - YEAR *3***  |
| **Timetable** | **Course Code** | **TA\_EAELE\_D - Year 3** |  |  |
| **DATE** | **DAY** | **SUBJECT** | **TIME** | **LOCATION**  |
| **19th August** | **Monday** | **Mathematics 6** | **15:30** | **146/148** |
| **20th August** | **Tuesday** | **Analysis of Analogue Communications** | **09:30** | **223/225** |
| **21st August** | **Wednesday** | **Electronic Circuits & Filters** | **12:30** | **138/140** |
| **22nd August** | **Thursday** | **Power Engineering** | **15:30** | **142/144** |
| **23rd August** | **Friday** | **Embedded Systems** | **09:30** | **040/042** |
| **24th August** | **Saturday** | **Digital Design with Verilog** | **09:30** | **040/042** |

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| **BACHELOR OF ENGINEERING (HONS) - ELECTRONIC ENGINEERING – AB INITIO – YEAR *3***  |
| ***Timetable*** | ***Course Code*** | **TA\_EAELE\_B - Year 3** |  |  |
| **DATE** | **DAY** | **SUBJECT** | **TIME** | **LOCATION** |
| **19th August** | **Monday** | **Mathematics 6** | **15:30** | **146/148** |
| **20th August** | **Tuesday** | **Analysis of Analogue Communications** | **09:30** | **223/225** |
| **21st August** | **Wednesday** | **Electronic Circuits & Filters** | **12:30** | **138/140** |
| **22nd August** | **Thursday** | **Power Engineering** | **15:30** | **142/144** |
| **23rd August** | **Friday** | **Embedded Systems** | **09:30** | **040/042** |
| **24th August** | **Saturday** | **Digital Design with Verilog** | **09:30** | **040/042** |

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| **BACHELOR OF ENGINEERING - ENGINEERING SOFTWARE - YEAR 3** |
| **Timetable** | **Course Code** | **TA\_ EAENS\_D Year 3** |  |  |
| **DATE** | **DAY** | **SUBJECT** | **TIME** | **LOCATION**  |
| **19th August** | **Monday** | **Mathematics 6** | **15:30** | **146/148** |
| **21st August** | **Wednesday** | **Management Techniques** | **15:30** | **050/052** |
| **23rd August** | **Friday** | **Embedded Systems** | **09:30** | **040/042** |
| **25th August** | **Saturday** | **Data Structures & Algorithms** | **15:30** | **138/140** |

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| **BACHELOR OF ENGINEERING (HONS) - ENGINEERING SOFTWARE - YEAR 3** |
| **Timetable** | **Course Code** | **TA\_ EAENS\_B Year 3** |  |  |
| **DATE** | **DAY** | **SUBJECT** | **TIME** | **LOCATION**  |
| **19th August** | **Monday** | **Mathematics 6** | **15:30** | **146/148** |
| **21st August** | **Wednesday** | **Management Techniques** | **15:30** | **050/052** |
| **23rd August** | **Friday** | **Embedded Systems** | **09:30** | **040/042** |
| **25th August** | **Saturday** | **Data Structures & Algorithms** | **15:30** | **138/140** |

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| **BACHELOR OF ENGINEERING (HONS) - ELECTRONIC ENGINEERING - YEAR 4**  |
| **Timetable** | **Course Code** | **TA\_EELEC\_B - Year 4** | **15 Minutes Reading Time** |
| **DATE** | **DAY** | **SUBJECT** | **TIME** | **LOCATION**  |
| **19th August** | **Monday** | **Mathematics 8** | **09:30** | **46** |
| **20th August** | **Tuesday** | **Digital Microprocessor Design -e** | **15:30** | **050/052** |
| **21st August** | **Wednesday** | **Operating Systems (e)** | **09:30** | **040/042** |
| **21st August** | **Wednesday** | **Management Practice** | **15:30** | **050/052** |
| **22nd August** | **Thursday** | **Digital Signal Processing**  | **09:30** | **050/052** |
| **23rd August** | **Friday** | **Nanoscale Devices (e)** | **09:30** | **138/140** |
| **23rd August** | **Friday** | **Network Engineering (e)** | **15:30** | **146/148** |
| **24th August** | **Saturday** | **Software Development 2 (e)** | **09:30** | **138** |
| **24th August** | **Saturday** | **Analogue IC Design -e** | **12:30** | **54** |
| **BACHELOR OF ENGINEERING (HONS) - ELECTRONIC ENGINEERING - YEAR 4**  |
| **Timetable** | **Course Code** | **TA\_EAELE\_B Year 4** | **15 Minutes Reading Time** |
| **DATE** | **DAY** | **SUBJECT** | **TIME** | **LOCATION**  |
| **19th August** | **Monday** | **Mathematics 8** | **09:30** | **46** |
| **20th August** | **Tuesday** | **Digital Microprocessor Design -e** | **15:30** | **050/052** |
| **21st August** | **Wednesday** | **Operating Systems (e)** | **09:30** | **040/042** |
| **21st August** | **Wednesday** | **Management Practice** | **15:30** | **050/052** |
| **22nd August** | **Thursday** | **Digital Signal Processing**  | **09:30** | **050/052** |
| **23rd August** | **Friday** | **Nanoscale Devices (e)** | **09:30** | **138/140** |
| **23rd August** | **Friday** | **Network Engineering (e)** | **15:30** | **146/148** |
| **24th August** | **Saturday** | **Software Development 2 (e)** | **09:30** | **138** |
| **24th August** | **Saturday** | **Analogue IC Design -e** | **12:30** | **54** |

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| **BACHELOR OF ENGINEERING (HONS) - ELECTRONIC ENGINEERING - YEAR 4**  |
| **Timetable** | **Course Code** | **TA\_ ECELE\_B Year 4** | **15 Minutes Reading Time** |
| **DATE** | **DAY** | **SUBJECT** | **TIME** | **LOCATION**  |
| **19th August** | **Monday** | **Mathematics 8** | **09:30** | **46** |
| **20th August** | **Tuesday** | **Digital Microprocessor Design -e** | **15:30** | **050/052** |
| **21st August** | **Wednesday** | **Operating Systems (e)** | **09:30** | **040/042** |
| **21st August** | **Wednesday** | **Management Practice** | **15:30** | **050/052** |
| **22nd August** | **Thursday** | **Digital Signal Processing**  | **09:30** | **050/052** |
| **23rd August** | **Friday** | **Nanoscale Devices (e)** | **09:30** | **138/140** |
| **23rd August** | **Friday** | **Network Engineering (e)** | **15:30** | **146/148** |
| **24th August** | **Saturday** | **Analogue IC Design -e** | **12:30** | **54** |
| **BACHELOR OF ENGINEERING (HONS) - ENGINEERING SOFTWARE - YEAR 4** |
| **Timetable** | **Course Code** | **TA\_EAENS\_B - Year 4** | **15 Minutes Reading Time** |
| **DATE** | **DAY** | **SUBJECT** | **TIME** | **LOCATION**  |
| **19th August** | **Monday** | **Mathematics 8** | **09:30** | **46** |
| **20th August** | **Tuesday** | **Machine Learning (e)** | **09:30** | **050/052** |
| **21st August** | **Wednesday** | **Operating Systems (e)** | **09:30** | **040/042** |
| **22nd August** | **Thursday** | **Internet of Things** | **09:30** | **050/052** |
| **23rd August** | **Friday** | **Network Engineering (e)** | **15:30** | **146/148** |
| **24th August** | **Saturday** | **Software Development 2 (e)** | **09:30** | **138** |

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| **BACHELOR OF ENGINEERING IN INDUSTRIAL ELECTRICAL ENGINEERING**  |
| **Timetable** | **Course Code** | **TA\_EIELE\_D (stage 1)** |  |
| **DATE** | **DAY** | **SUBJECT**  | **TIME** | **LOCATION**  |
| **21st August** | **Wednesday** | **Electrical Machines** | **15:30** | **050/052** |

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| **BACHELOR OF ENGINEERING IN INDUSTRIAL ELECTRICAL ENGINEERING**  |
| **Timetable** | **Course Code** | **TA\_EIELE\_D (stage 2)** |  |
| **DATE** | **DAY** | **SUBJECT**  | **TIME** | **LOCATION** |
| **19th August** | **Monday** | **Process Instrumentation & Calibration** | **19:00** | **138/140** |

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| **HIGHER CERTIFICATE IN ENGINEERING IN TELECOMMUNICATIONS & DATA NETWORKS**  |
| **Timetable** | **Course Code** | **TA\_ ETELE\_C** |  |
| **DATE** | **DAY** | **SUBJECT**  | **TIME** | **LOCATION** |
| **21st August** | **Wednesday** | **Broadband & Wireless Skills** | **12:30** | **46** |
| **24th August** | **Saturday** | **Network Installation & Health and Safety** | **12:30** | **54** |

**Please ensure you go to the room/area allocated for your course code on your ID Card**